

PATENT 5298-04700/PM00028

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of:

Gilboa et al.

Serial No. 09/846,119

Filed: April 30, 2001

For: METHOD OF MAKING A

PLANARIZED SEMICONDUCTOR

STRUCTURE

Group Art Unit: 1763 Examiner: Goudreau, G.

Atty. Dkt. No. 5298-04700

Thereby certify that this correspondence is being transmitted via facsimile to USPTO, C2800. Before Final,

Cevin L. Daffer

703/872-9318 on the date indicated below:

March 5, 2003

Date

AMENDMENT; RESPONSE TO OFFICE ACTION MAILED FEBRUARY 5, 2003

Box Non-Fee Amendment

Assistant Commissioner of Patents and Trademarks Washington, D.C. 20231

Dear Sir/Madam:

MAK 3 2003

TECHNOLOGY CENTER 2800

Responsive to the Office Action mailed February 5, 2003, please amend the case as follows:

IN THE CLAIMS

Please cancel claims 17, 18 and 28 without prejudice or disclaimer as to the subject matter recited therein. Applicants reserve the right to file a divisional application at a later date capturing the subject matter recited in claims 17, 18 and 28 canceled herein.

REMARKS

The present response cancels claims 17, 18 and 28 in conformity with the following remarks. Claims 1-6, 19, 22-27, 29 and 30 remain pending in the captioned case.

GROUP 1700

Page 1 of 2

SN 09/846,119 Response to Office Action mailed 2/5/02